

TRANSMITTAL FORM

Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: BUR920000228



DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS

First Named Inventor: Mr. John C. Malinowski

SUBMITTED BY

Name: Howard J. Walter Jr.
Registration Number: 24832
Electronic Signature Mark: Howard J.
Walter, Jr. /s/ Date Signed: 20011109

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Attached Files:

bibd-transmittal	BUR920000228US1apds.xml
fee-transmittal	BUR920000228US1fee.xml
patent-assignment	BUR920000228US1asgn.xml

specification

BUR920000228US1.xml

declaration

BUR920000228US1Dec1.tif

declaration

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declaration

BUR920000228US1Dec3.tif

declaration

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Attached Image File(s):

BUR920000228US1Dec1.tif

BUR920000228US1Dec2.tif

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Comments:

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PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

11/13/2001 EFLORES 00000053 090456 09683027

01 FC:101	740.00 CH
02 FC:102	588.00 CH
03 FC:103	360.00 CH

FEE TRANSMITTAL

Electronic Version 1.1.0

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TOTAL FEES AUTHORIZED: \$ 1728

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Electronic Signature Mark: Howard J. Walter, Jr./s/
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BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 740

Subtotal For Basic Filing Fee: \$ 740

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 40	103	\$ 18	20	\$ 360
Independent Claims: 10	102	\$ 84	7	\$ 588

Subtotal For Extra Claims Fees: \$ 948

ADDITIONAL FEES

Fee Description	Fee Code	Fee Paid
Recording Each Patent Assignment Per Property Fee	581	\$ 40

Subtotal For Additional Fees: \$ 40

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